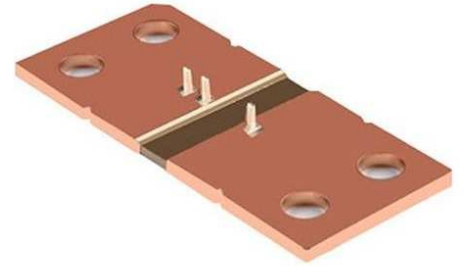


TCSN series Current Sensing Resistors

These components are four-terminal, bus-bar, metal strip current shunts. Assembled using electron beam welding. Also they can absorb a high pulse power rating and have very low inductance. They also feature excellent long term stability, less than 100ppm/°C TCR, and have excellent frequency characteristics. Applications include : Battery charging current control of automotive electronics, current detection in precise power sources, constant current sources, industrial power conversion circuits, HEVs, fuel cells and constant electronic loads.



GENERAL SPECIFICATIONS

Model	*Max Power Rating [W] (*At terminal temp. ≤ 140°C)	*Resistance [Ω]	Max. T.C.R	Continuous current [A] at 0.025mΩ (*At terminal temp. ≤ 140°C)	Resistance Tolerance
TCSN8536	Max 50	0.025m	± 150ppm/°C	1200	D [±0.5%]
		0.05m	± 100ppm/°C		F [±1%]
		0.1m	± 100ppm/°C		J [±5%]

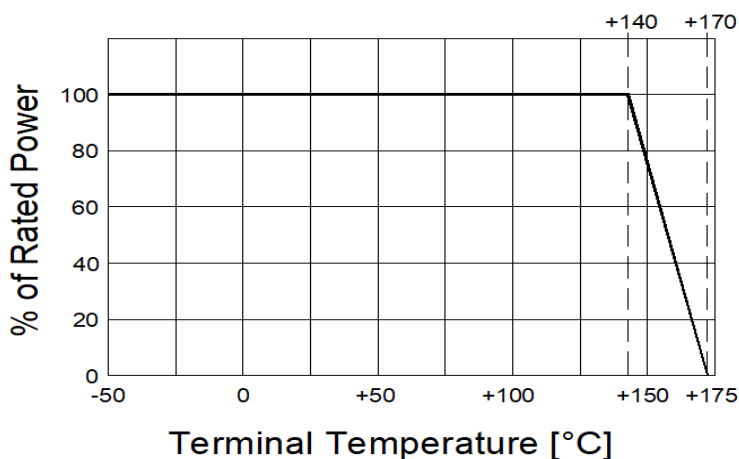
* Referred to power derating curve

* Also available in extended ohmic detail info. Contact RARA

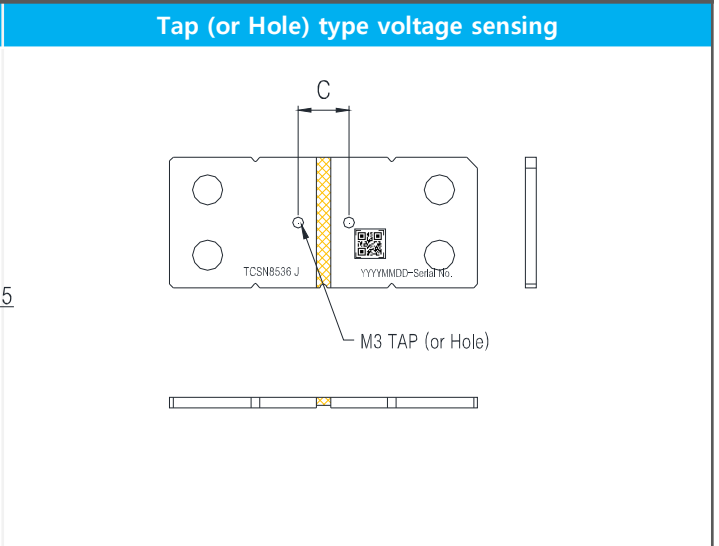
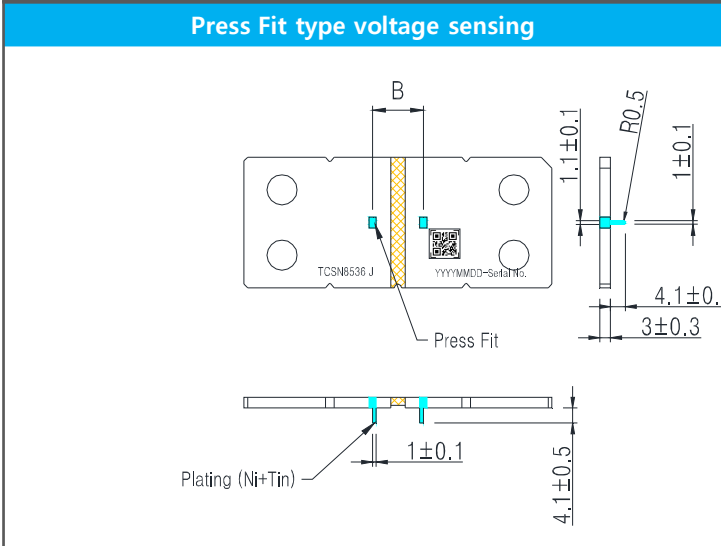
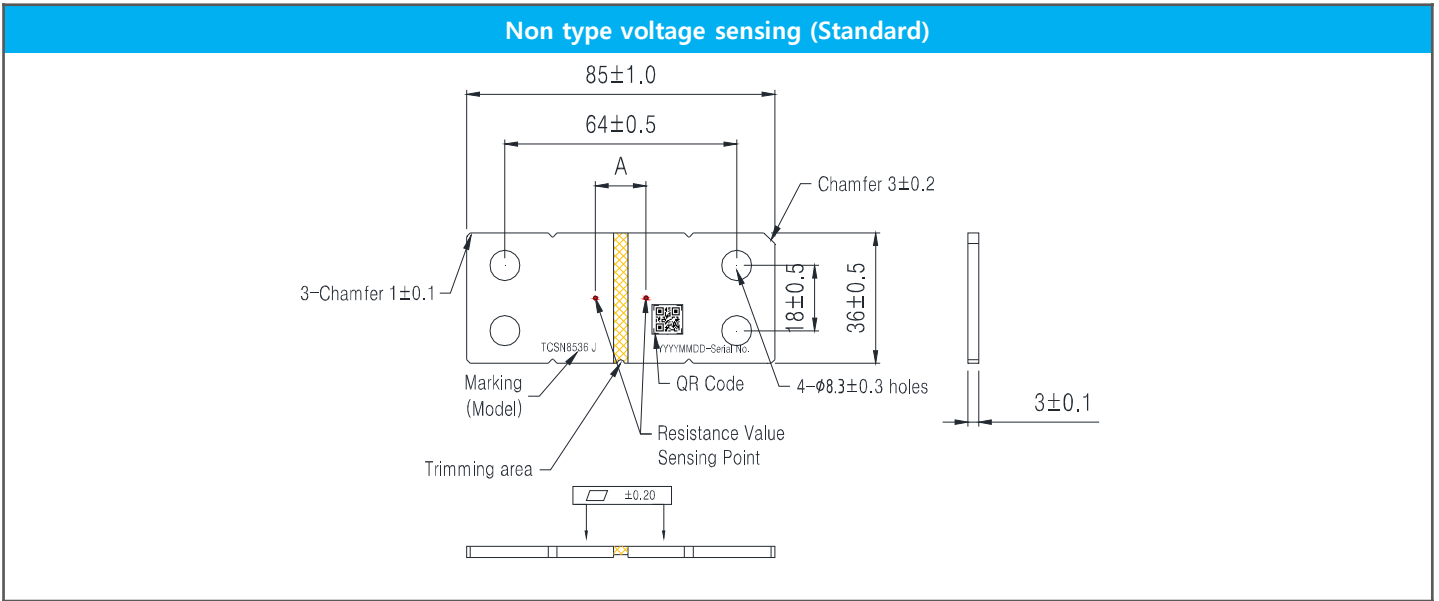
CHARACTERISTICS

Test	Condition
Operating Temperature	-55°C ~ +170°C
Thermal Resistance (Rthi)	2K / W
Thermal EMF (0-100°C)	<1μV/°C
Inductance	<5nH

DERATING CURVE



DIMENSIONS (mm)



Model	Resistance [mΩ]	Dimensions [mm]		
		A±0.5	B±0.5	C±0.5
TCSN8536	0.025	7	9	10
	0.05	10	13	14
	0.1	18	19	22

Marking Example & QR Code Example

Marking Example					QR Code Example			
TCSN8536 J 20260506 00001					RARA20260506-0001R49890n			
Model	Size	Tolerance	Production date	Serial No.	Supplier	Lot No.	Batch No.	R49890n
TCSN	8536	J	20260506	00001	RARA Electronics	YYYYMMDD	0001	Resistance Value(nΩ)

PERFORMANCE

Test	Condition	
TCR	Within spec	IEC60115-1 4.8, measured point -40°C ~ +140°C, reference point +25°C
Solder Heat Resistance	No visible damage ΔR±0.5% Maximum	IEC60115-1 4.18 , 260°C tin bath , 10s
Load life	No visible damage ΔR±1% Maximum	IEC60115-1 4.25.1 , 1000hours at 70°C±2°C , with rated current or component limit current (whichever is lower), 1.5h on / 0.5h of cycle
High temp. & High humidity	No visible damage ΔR±1% Maximum	MIL-STD-202 Method 103, 10 hours at 85°C and 85% RH, with 10% rated power (current) or component limit current (whichever is lower) applied.
Temperature cycle	No visible damage ΔR±1% Maximum	IEC60115-1 4.19 , -55°C@30min ~ +155°C@30min ; 1000cycles
High temp. storage	No visible damage ΔR±1% Maximum	IEC60115-1 4.25.3 , 1000hours at 170°C, without loading current and voltage
Low Temp. Load	No visible damage ΔR±0.5% Maximum	IEC60115-1 4.36 , col from rom temperature to -45°C, 1.5h no load, then apply rated power for 45min, col for 15min, return to room temperature for testing.
Vibration Test	No visible damage ΔR±0.5% Maximum	MIL-STD-202 Method 204 Peak acceleration: 5g (gravitational acceleration) Frequency range: (10~2000Hz) Test directions: X, Y Z axes, 12 cycles per axis, 20min per cycle, total ~12h
Shock Test	No visible damage ΔR±0.5% Maximum	MIL-STD-202 Method 213 Shock acceleration : 100g (gravitational acceleration) Pulse width : 6ms Waveform: half-sine wave Shock directions: ±X, ±Y, ±Z axes, 3 times per direction

ORDERING PROCEDURE EXAMPLE

